

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Taravade et al.)	Group Art Unit: To be assigned
Attorney Docket No. 1003-0637)	
Application No.: To be assigned)	Examiner: To be assigned
Filed: Herewith)	
For: Method Of Fabricating An Integral)	
Capacitor And Gate Transistor)	
Having Nitride And Oxide Polish)	
St Layers Using Chemical)	
Mechanical Polishing Elimination)	

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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PRELIMINARY AMENDMENT

Sir:

Prior to examination, kindly enter the following amendments:

Amendments to the Claims begin on page 2 of this paper.